



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Richard L. Coulson et al.

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Art Unit: 2863

Serial No.: 10/660,310

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Examiner: Aditya S. Bhat

Filed: September 11, 2003

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Docket: ITL.1029US
P16765

For: Adaptive Cache Algorithm for
Temperature Sensitive Memory

§

Assignee: Intel Corporation

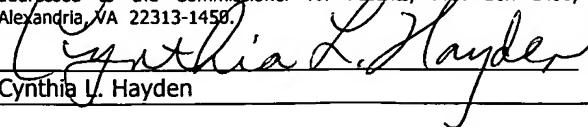
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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 20060307

Sir:

In response to the office action mailed March 14, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: <u>May 16, 2006</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden